

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,813,214 B2
DATED : November 2, 2004
INVENTOR(S) : Cho et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7,

Line 30, replace "transistors M1_O, M2_, M3_O" with -- transistors M1_O, M2_O, M3_O --.

Line 31, replace "transistor M6_," with -- transistor M6_O --.

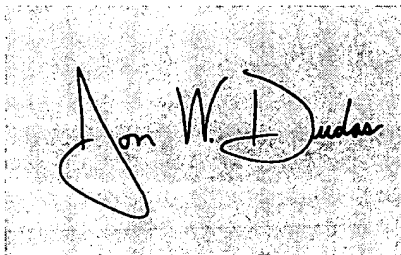
Line 34, replace "transistors M1_, M2_O" with -- transistors M1_O, M2_O --.

Column 13,

Line 43, replace "transistors MS_E" with -- transistors M8_E --.

Signed and Sealed this

Twenty-seventh Day of December, 2005

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area that appears to be a stamp or seal. The signature is stylized, with the first name "Jon" and last name "Dudas" clearly legible, and "W." in the middle.

JON W. DUDAS

Director of the United States Patent and Trademark Office